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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/941,701	08/30/2001	08/30/2001 Katsuyuki Oohara		6227	
23850 75	590 06/17/2004		EXAMINER		
ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP			CHANG, ERIC		
1725 K STREE SUITE 1000	T, NW		ART UNIT	PAPER NUMBER	
	ASHINGTON, DC 20006		2116	3	
			DATE MAILED: 06/17/2004	4	

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Applicat	tion No.	Applicant(s)	
	. ,	09/941,	701	OOHARA ET AL.	
	Office Action Summary	Examine	ər	Art Unit	
		Eric Cha	ang	2116	
Period f	The MAILING DATE of this communic for Reply	cation appears on th	ne cover sheet with	the correspondence address	
THE - Extra afte - If th - If N - Fail Any	HORTENED STATUTORY PERIOD FO MAILING DATE OF THIS COMMUNIO ensions of time may be available under the provisions of er SIX (6) MONTHS from the mailing date of this commu- ne period for reply specified above is less than thirty (30) O period for reply is specified above, the maximum state lure to reply within the set or extended period for reply we or reply received by the Office later than three months aftended patent term adjustment. See 37 CFR 1.704(b).	CATION. f 37 CFR 1.136(a). In no e inication. I days, a reply within the st utory period will apply and rill, by statute, cause the ap	event, however, may a reply atutory minimum of thirty (3 will expire SIX (6) MONTH oplication to become ABAN	y be timely filed 30) days will be considered timely. S from the mailing date of this communication. DONED (35 U.S.C. § 133).	
Status					
1)[Responsive to communication(s) filed	l on 30 August 200) 1 .		
2a)□		b) This action is			
3)□	Since this application is in condition for	or allowance excep	ot for formal matters	s, prosecution as to the merits is	
	closed in accordance with the practic	e under <i>Ex part</i> e Q	<i>uayl</i> e, 1935 C.D. 1	1, 453 O.G. 213.	
Disposit	tion of Claims				
5)□ 6)⊠ 7)□	Claim(s) <u>1-5</u> is/are pending in the app 4a) Of the above claim(s) is/are Claim(s) is/are allowed. Claim(s) <u>1-5</u> is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restrict	e withdrawn from o			
Applicat	tion Papers				
10)⊠	The specification is objected to by the The drawing(s) filed on 30 August 200 Applicant may not request that any object Replacement drawing sheet(s) including the oath or declaration is objected to	<u>01</u> is/are: a)⊠ acco ion to the drawing(s) he correction is requi	be held in abeyance ired if the drawing(s)	. See 37 CFR 1.85(a). is objected to. See 37 CFR 1.121(d).	
Priority	under 35 U.S.C. § 119				
a)	Acknowledgment is made of a claim for the priority of the priority of the priority of the priority of the copies of the priority of the certified copies of the priority of the certified copies of the certified copies of application from the Internation the set the attached detailed Office action	ocuments have be ocuments have be f the priority docum al Bureau (PCT Ru	en received. en received in App nents have been re ule 17.2(a)).	lication No ceived in this National Stage	
2) 🔲 Notic 3) 🔯 Infor	nt(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PT rmation Disclosure Statement(s) (PTO-1449 or P er No(s)/Mail Date <u>3</u> .			imary (PTO-413) Iail Date mal Patent Application (PTO-152)	

Application/Control Number: 09/941,701 Page 2

Art Unit: 2116

DETAILED ACTION

1. Claims 1-5 are pending.

Specification

The abstract of the disclosure is objected to because of the incorporation of figure 2. reference numbers. Correction is required. See MPEP § 608.01(b).

Claim Objections

- 3. Claims 3 and 4 are objected to because of the following informalities: they refer to "other control unit" and "other control units" in lines 3 of their respective claims. However, lines 1-2 of claim 1 on which claims 3 and 4 depend only provides for "a control unit for executing data communication between itself and another control unit". Appropriate correction is required.
- 4. Claim 3 is objected to because of the following informalities: it is unclear what the phrase "even the" on line 4 of the claim means. Appropriate correction is required.

Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Application/Control Number: 09/941,701

Art Unit: 2116

- 6. Claims 1-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent 6,266,780 to Grundvig et al., in view of U.S. Patent 6,311,296 to Congdon, and in further view of U.S. Patent 6,459,705 to Cheng.
- 7. As to claim 1, Grundvig discloses a control unit for comprising: a CPU [col. 1, lines 7-10]; a high frequency oscillator for producing first clock pulses for operating the CPU at a first frequency [col. 1, lines 20-23]; a low frequency oscillator for producing second clock pulses for operating the CPU at a second frequency which is lower than said first frequency [col. 1, lines 23-25]; and an exchanging means for exchanging clock pulses for operating the CPU from said first clock pulses to said second clock pulses when a prescribed condition is satisfied, thereby shifting the CPU to a low power consumed state [col. 1, lines 31-35]. Grundvig teaches all of the limitations of the claim, but does not teach that errors in the operation of the low power oscillator are detected.

Congdon teaches an abnormality detecting means for detecting abnormality of a clock oscillator [col. 12, lines 55-67]. Although Congdon specifically teaches detecting abnormalities in the operation of a bus clock, it would be obvious that the teachings of Congdon be applied to detecting abnormalities in other oscillators in relationship to a reference clock, substantially as claimed.

At the time that the invention was made, it would have been obvious to a person of ordinary skill in the art to employ the clock abnormality detection as taught by Congdon. One of

Application/Control Number: 09/941,701

Art Unit: 2116

ordinary skill in the art would have been motivated to do so to detect if a clock oscillator was malfunctioning.

It would have been obvious to one of ordinary skill in the art to combine the teachings of the cited references because they are both directed to the problem of using multiple clock systems in a computer. Moreover, the clock abnormality detection means taught by Congdon would improve the robustness of Grundvig because it allowed errors with clocks in other parts of the computer system, such as the bus, to be detected.

Grundvig and Congdon teach all of the limitations of the claim, but do not teach that exchange stopping means for stopping exchange of the clock pulses by said exchanging means if the said abnormality detecting means detects the abnormality.

Cheng teaches that the CPU is brought out of the low power mode if such an error in the oscillator was detected [col. 1, lines 51-57].

At the time that the invention was made, it would have been obvious to a person of ordinary skill in the art to employ the CPU waking mechanism as taught by Cheng. One of ordinary skill in the art would have been motivated to do so that the CPU can be monitored during a low-power state.

It would have been obvious to one of ordinary skill in the art to combine the teachings of the cited references because they are both directed to the problem of monitoring for problems with a computer system in a low-power state. Moreover, the CPU waking means taught by Cheng would improve the utility of Grundvig and Congdon because it allowed the computer to report a change in status during a low-power mode to a user or other supervisory entity.

Application/Control Number: 09/941,701 Page 5

Art Unit: 2116

8. As to claim 2, Congdon discloses said abnormality detecting means includes a counting means for counting the second clock pulses produced from said oscillator while said CPU is operated at the first frequency [col. 12, lines 50-67, and col. 13, lines 24-67]. Congdon teaches that counting means are used to determine if an oscillator abnormality is detected in relation to a reference clock, substantially as claimed.

- 9. As to claims 3-4, Cheng discloses said exchange stopping means sends an exchange request signal to other control units so that they are shifted into the low power consumed state even if the CPU corresponding to said exchange stopping means cannot be shifted into the low power consumed state [col. 1, lines 51-56]. Cheng teaches that the components of a computer system can be placed into a low-power state even in the event of a detected error, such as an oscillator anomaly, substantially as claimed.
- 10. As to claim 5, Grundvig, Congdon, and Cheng discloses a control unit for placing a CPU into a low-power mode by switching to a low-frequency oscillator, and waking the CPU from the low-power mode if an anomaly in the low-frequency oscillator is detected, substantially as claimed. Because Grundvig, Congdon, and Cheng teach the control unit, they teach a multiplex communication system comprising such control units, substantially as claimed.

Conclusion

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eric Chang whose telephone number is (703) 305-4612. The examiner can normally be reached on M-F 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne Browne can be reached on (703) 308-1159. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ec May 7, 2004

> REHANA PERVEEN PRIMARY EXAMINER